

Digital Circuits

ECS 371

Dr. Prapun Suksompong

prapun@siit.tu.ac.th

Lecture 7

Office Hours:

BKD 3601-7

Monday 9:00-10:30, 1:30-3:30

Tuesday 10:30-11:30

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Announcement

- HW2 posted on the course web site
 - Chapter 4:
 - **Write down all the steps** that you have done to obtain your answers.
 - Due date: July 9, 2009 (Thursday)
 - Please submit your HW to the instructor 3 minutes BEFORE your class starts.
 - Late submission will NOT be accepted.
 - Earlier submission is possible. There are two HW boxes in the EC department (6th floor) for ECS 371. (One for CS. Another one for IT.)

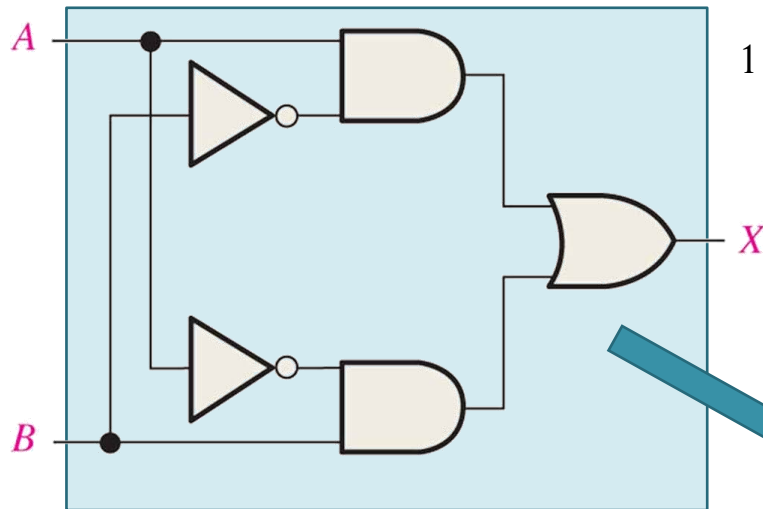
Announcement

- Score and solution for HW1 posted on the course web site.
- Reading Assignment
 - Chapter 4: 4.1 to 4-9
 - The textbook contains *much more explanation* than what I can talk about in class. Reading it will help you understand our material from another perspective.
- Today, we will use
 - Handout for lecture 7 (new one)
 - Handout for lecture 5-6 (old one)

Our Goals

- Ultimate goals:
 1. Analyze digital circuits.
 2. Design digital circuits to do something useful.
- 1. Analyze digital circuit:
 - Given a digital circuit, find out what it does.
 - One way to achieve this goal is to look at the truth table.

Example: Analyzing Logic Circuit



1. Suppose we are given a digital circuit.

2. Analyze it via the truth table

A	B	X
0	0	0
0	1	1
1	0	1
1	1	0

$$X = A \cdot \bar{B} + \bar{A} \cdot B$$

It may be useful to find a Boolean expression first.

3. Interpretation: This is a circuit that checks whether $A \neq B$.

Output X ~~= 0~~ if A and B are not the same.

Output X ~~= 1~~ if A is the same as B.

Sometimes, we may be able to simplify the circuit by simplifying its Boolean expression.

We can apply the same technique to more complicated circuits.

Our Goals

- Ultimate goals:

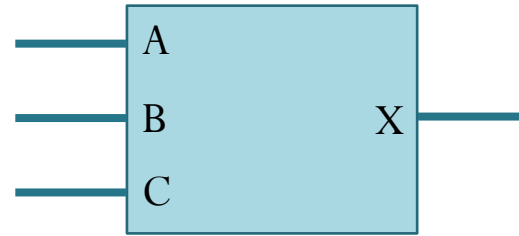
1. Analyze digital circuits.
2. Design digital circuits to do something useful.

1. Analyze digital circuit:

2. Design digital circuits:

- Given a task, build a SIMPLE digital circuit that perform the specified task.
- A recipe:
 - Directly determine a Boolean expression corresponding to the task or start with a truth table and then turn it into a Boolean expression.
 - Simplify the expression. ← Many examples last week. (One in next slide.)
 - Construct a circuit from the simplified expression.

Example



$$X = \overline{A}\overline{B}C + \overline{A}B\overline{C} + \overline{A}B\overline{C} + \overline{A}\overline{B}\overline{C} + A\overline{B}\overline{C}$$

$$\begin{aligned} & \overline{A}\overline{B}C + \overline{A}B\overline{C} + \overline{A}\overline{B}\overline{C} + \overline{A}\overline{B}\overline{C} + A\overline{B}\overline{C} \\ &= \overline{A}\overline{B}C + (\overline{A}\overline{B}C + \overline{A}\overline{B}\overline{C} + \overline{A}\overline{B}\overline{C} + \overline{A}\overline{B}\overline{C}) \\ &= \overline{A}\overline{B}C + \overline{B}(AC + \overline{A}C + \overline{A}\overline{C} + A\overline{C}) \\ &= \overline{A}\overline{B}C + \overline{B} \\ &= \overline{A}C + \overline{B} \end{aligned}$$

Direct construction of a circuit from this equation will be complicated.

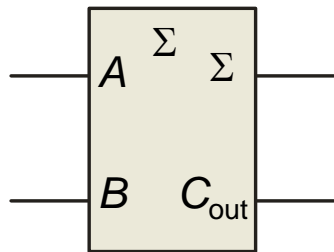
Simplification

Construction of an equivalent circuit from this equation is simpler.

$$X = \overline{A}C + \overline{B}$$

Example: Designing Logic Circuit

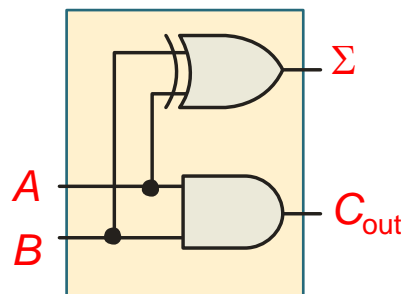
1. Suppose we want to build a circuit that perform a one-bit addition



2. We can summarize its task by a truth table.

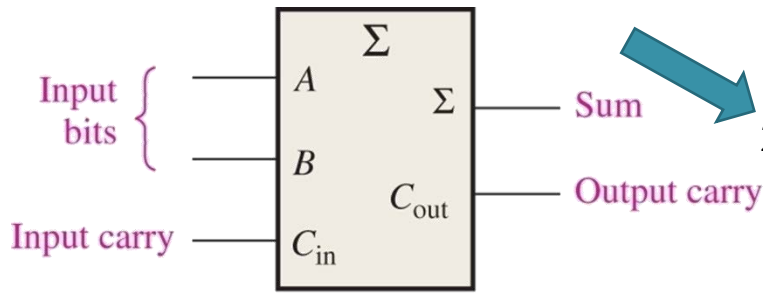
Inputs		Outputs	
A	B	C_{out}	Σ
0	0	0	0
0	1	0	1
1	0	0	1
1	1	1	0

3. Can we turn this truth table into a simple circuit?



Example: Designing Logic Circuit (2)

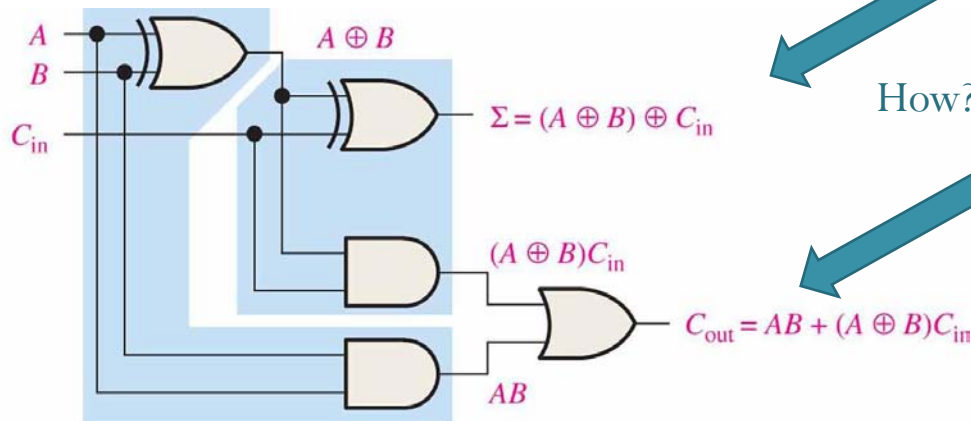
1. Suppose we want to build a circuit that perform a one-bit addition with input carry



2. We can summarize its task by a truth table.

Inputs			Outputs	
A	B	C _{in}	C _{out}	Σ
0	0	0	0	0
0	0	1	0	1
0	1	0	0	1
0	1	1	1	0
1	0	0	0	1
1	0	1	1	0
1	1	0	1	0
1	1	1	1	1

3. Can we turn this truth table into Boolean expression?



How?

Truth Table Boolean Expression?

- How can we turn a truth table into Boolean expression?
- Let's first study the relationship between truth table and Boolean expression.
- It is easier to convert a Boolean expression into a truth table.
- There are some forms of Boolean expressions that are easily converted into their corresponding truth tables.
 - Sum-of-products form
 - Product-of-sums form

Product Term

A single literal or a product of two or more literals.

Example: $A \cdot \bar{B} \cdot C$

$A \cdot C$

A

$A \cdot \bar{B} \cdot C \cdot D$

$\bar{A} \cdot \bar{B} \cdot \bar{C}$

Caution:

$\overline{A \cdot B \cdot C}$ is not a product term.

Q : When does $A \cdot \bar{B} \cdot C = 1$?

A : IFF $(A, B, C) = (1, 0, 1)$

So, it is very easy to construct a truth table for

$$X = A \cdot \bar{B} \cdot C$$

A	B	C	X
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	0
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	0

Sum Term

A single literal or a sum of two or more literals.

Example:

$$A + \bar{B} + C$$

$$A + C$$

$$A$$

$$A + \bar{B} + C + D$$

$$\bar{A} + \bar{B} + \bar{C}$$

Caution:

$\overline{A + B + C}$ is not a sum term.

Q: When does $A + \bar{B} + C = 1$?

It might be easier to ask:

Q: When does $A + \bar{B} + C = 0$?

A: IFF $(A, B, C) = (0, 1, 0)$

Q: When does $A + \bar{B} + C = 1$?

A: IFF $(A, B, C) \neq (0, 1, 0)$

It is easy to construct a truth table for

$$X = A + \bar{B} + C$$

A	B	C	X
0	0	0	1
0	0	1	1
0	1	0	0
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	1

Again

1. It is easy to find the case when a product term = 1.

$$A \cdot \bar{B} \cdot C = 1 \text{ iff } (A, B, C) = (1, 0, 1)$$

2. It is easy to find the case when a sum term = 0.

$$A + \bar{B} + C = 0 \text{ iff } (A, B, C) = (0, 1, 0)$$

Sum-of-Product (SOP) Form

A product term or a sum of product terms

Example: $A\bar{B} + ABC$

$$AB + C$$

$$ABC + CDE$$

Example: Develop a truth table for $X = A\bar{B} + ABC$

Observe that when a product term does not contain all variables, it corresponds to multiple 1s in the output column of the truth table. This is because the missing variable does not affect the product term and hence can be 0 or 1.

A	B	C	X
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	0
1	1	1	1

Observe that when a product term contains all the variables, it corresponds to a unique 1 in the output column of the truth table.

Standard SOP (Canonical Sum)

Each product term contains all variables.

Example:

Nonstandard
product
term

$\overline{A}\overline{B} + ABC$ is not a standard SOP expression.

$\overline{A}\overline{B}C + \overline{A}B\overline{C} + ABC$ is a standard SOP expression.

Minterm: A product term in a standard SOP expression.

Example: $\overline{A}\overline{B}C + \overline{A}B\overline{C} + ABC$ contains 3 minterms

Observe these!

- A minterm is equal to 1 for only one combination of variable values.
- A minterm corresponds to a unique 1 in the output column of the truth table.
- A minterm corresponds to a unique row of the truth table.
- A canonical sum is a sum of minterms.

Remember these:

- **SOP** = Sum of products
- *Standard SOP* = Canonical sum
- **Minterm** = A product term in canonical sum


Domain

Domain of an expression:

Set of variables contained in the expression.

Example: Domain of $A\bar{B} + ABC$ is $\{A, B, C\}$.

So, a **standard** SOP expression is one in which all the variables in the domain appear in each product term in the expression.



Alternative definition for a standard SOP expression.

Row Number of the Truth Table

Row #	A	B	C	X
0	0	0	0	
1	0	0	1	
2	0	1	0	
3	0	1	1	
4	1	0	0	
5	1	0	1	
6	1	1	0	
7	1	1	1	

Think of these as one unsigned binary number. The corresponding decimal value is the row #.

Minterm/Row Number & Truth Table

Row #	A	B	C	Minterm
0	0	0	0	$\overline{A} \cdot \overline{B} \cdot \overline{C}$
1	0	0	1	$\overline{A} \cdot \overline{B} \cdot C$
2	0	1	0	$\overline{A} \cdot B \cdot \overline{C}$
3	0	1	1	$\overline{A} \cdot B \cdot C$
4	1	0	0	$A \cdot \overline{B} \cdot \overline{C}$
5	1	0	1	$A \cdot \overline{B} \cdot C$
6	1	1	0	$A \cdot B \cdot \overline{C}$
7	1	1	1	$A \cdot B \cdot C$

Summary: Each row of the truth table corresponds to

1. A combination of input (A,B,C).
2. A row #.
3. A minterm.

Binary Representation

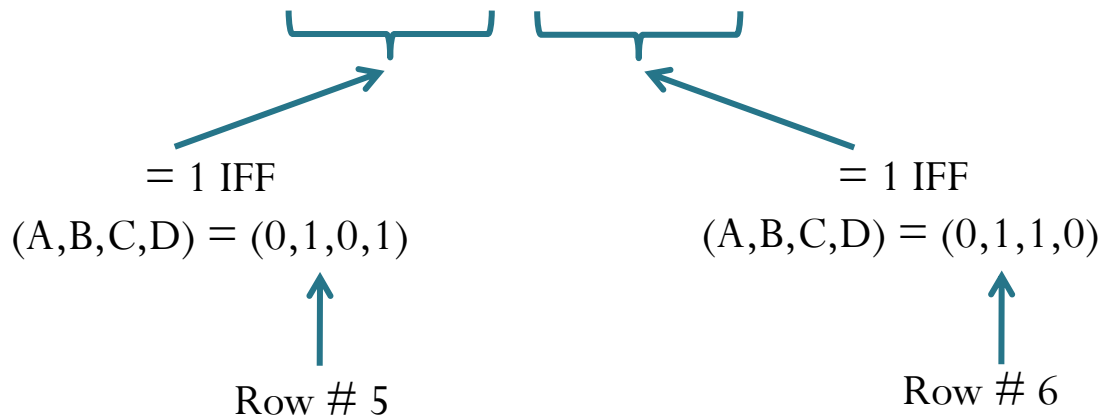
(of minterm and of any Boolean expression)

Example: Consider a canonical sum:

$$X = \overline{A}B\overline{C}D + \overline{A}BC\overline{D}$$

A	B	C	D	X
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	0
0	1	0	1	1
0	1	1	0	1
0	1	1	1	0
1	0	0	0	0
1	0	0	1	0
1	0	1	0	0
1	0	1	1	0
1	1	0	0	0
1	1	0	1	0
1	1	1	0	0
1	1	1	1	0

$$\overline{A}B\overline{C}D + \overline{A}BC\overline{D}$$



$$\overline{A}B\overline{C}D + \overline{A}BC\overline{D} = \sum_{A,B,C,D} (5,6)$$

This is called a **minterm list**

It means “the sum of minterms 5,6 with variable A, B, C, D”

Canonical Sum and Minterm List

- Example: Convert $A\bar{B}C + \bar{A} \cdot \bar{B}$ into canonical sum.


$$\begin{aligned}\text{Hint : } X &= X \cdot 1 = X \cdot (Y + \bar{Y}) \\ &= X \cdot Y + X \cdot \bar{Y}\end{aligned}$$

- Example: Convert $A\bar{B}C + \bar{A} \cdot \bar{B}$ into a minterm list.
- Any logic function/expression can be expressed as a canonical sum (which is equivalent to a minterm list).

Truth Table Boolean Expression

Add all the minterms corresponding to a 1 in the output column of the truth table.

A	B	C	X
0	0	0	1
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	1
1	0	1	0
1	1	0	1
1	1	1	1


$$X = \bar{A} \cdot \bar{B} \cdot \bar{C} + \bar{A} \cdot B \cdot C + A \cdot \bar{B} \cdot \bar{C} \\ + A \cdot B \cdot \bar{C} + A \cdot B \cdot C$$

Alternatively, we may write

$$X = \sum_{A,B,C} (0,3,4,6,7)$$

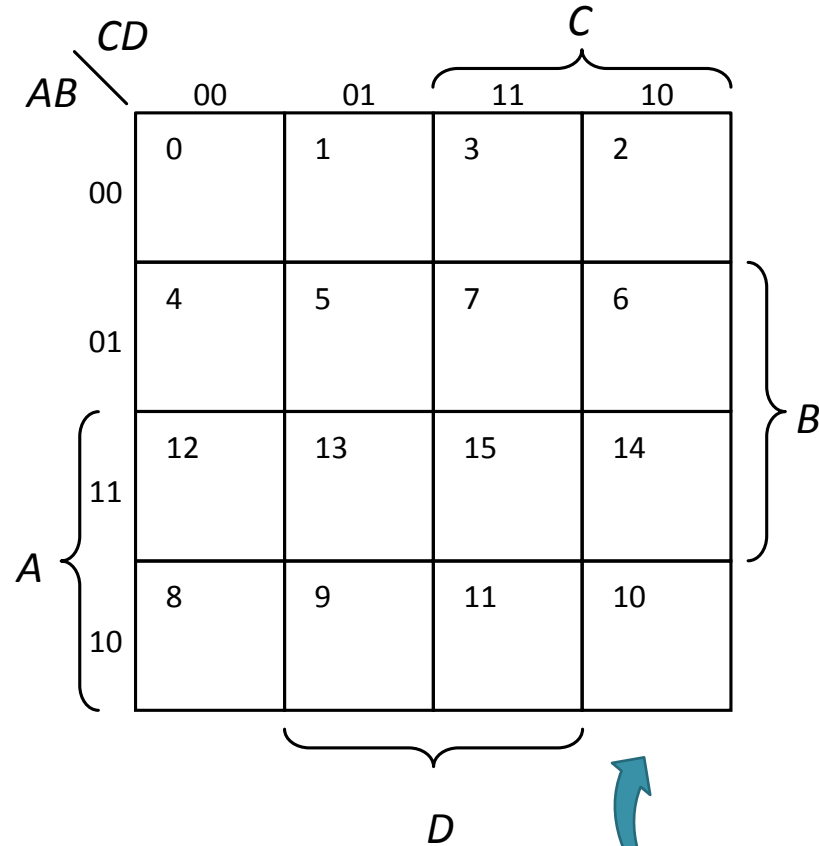
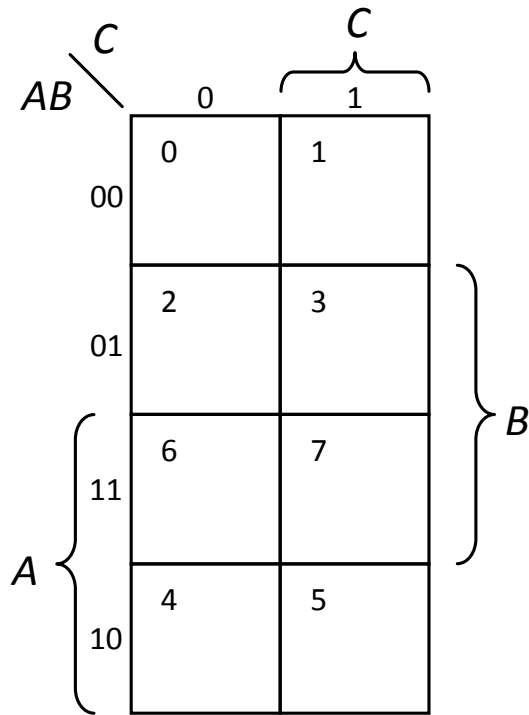
Minimization (A Revisit!)

“The process that results in an expression containing the fewest possible terms with the fewest possible variables.”

We have done some minimization using Boolean algebra.

The expression can then be implemented with fewer logic gates.

Karnaugh Map



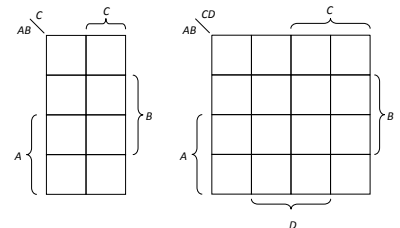
Row #	A	B	C
0	0	0	0
1	0	0	1
2	0	1	0
3	0	1	1
4	1	0	0
5	1	0	1
6	1	1	0
7	1	1	1

Row #	A	B	C	D
0	0	0	0	0
1	0	0	0	1
2	0	0	1	0
3	0	0	1	1
4	0	1	0	0
5	0	1	0	1
6	0	1	1	0
7	0	1	1	1
8	1	0	0	0
9	1	0	0	1
10	1	0	1	0
11	1	0	1	1
12	1	1	0	0
13	1	1	0	1
14	1	1	1	0
15	1	1	1	1

The small number inside each cell is the corresponding row number in the truth table, assuming that the truth table inputs are labeled alphabetically from left to right (e.g. A, B, C) and the rows are numbered in binary counting order.

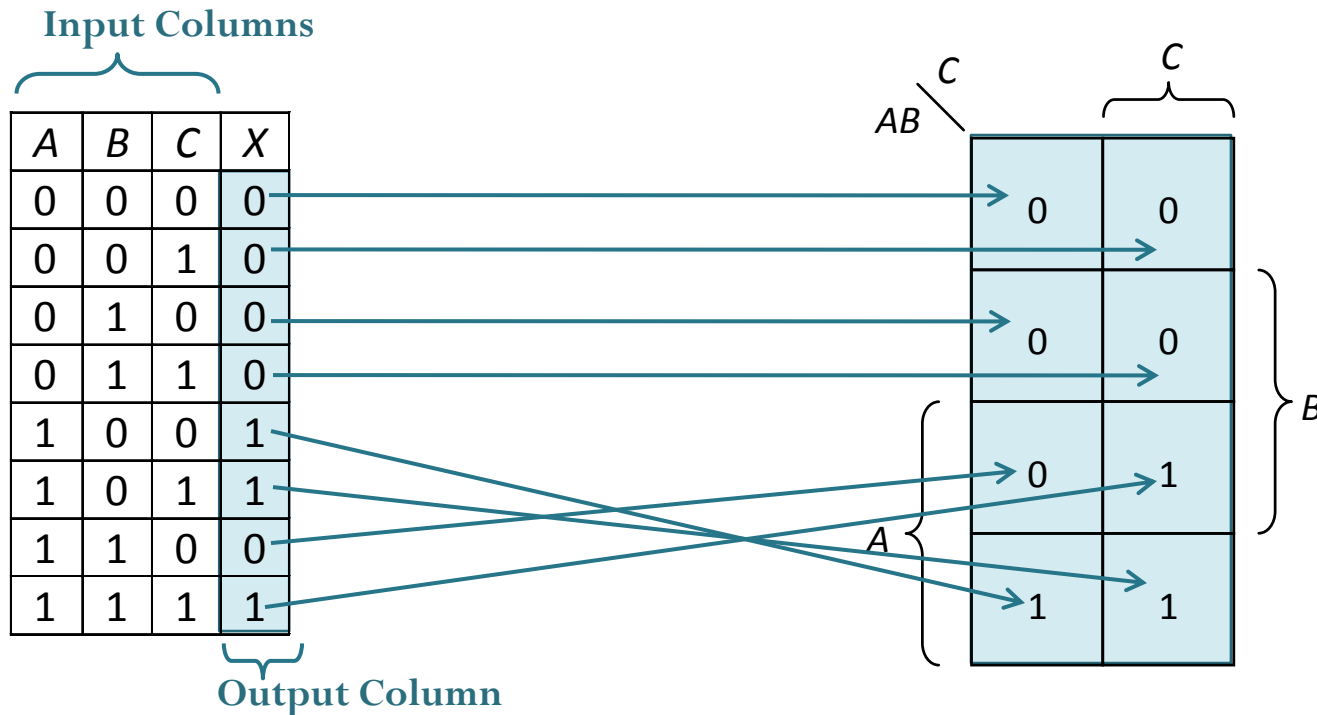
Karnaugh Map: Basic Layout

- Similar to a truth table.
 - Present all of the possible values of input variables and the resulting output for each value.
- Instead of being organized into columns and rows, it is an **array of cells**.
- The number of cells in a K-map, as well as the number of rows in a truth table, is equal to the total number of possible input variable combinations (which is the same as the total number of minterms).
- The cells are arranged in a way so that simplification of a given expression is simply a matter of properly grouping the cells.
- Used for simplifying Boolean expressions to their “minimum form”.



Example: Truth Table v.s. K-map

$$X = A\bar{B} + ABC$$



It can be difficult (especially for large K-map) to remember which cell in K-map corresponds to a particular row in the truth table.

We also want to get the K-map directly from Boolean expression without writing down truth table first.

The 2-variable Karnaugh Map (2x2 K-Map)

We will study K-maps for logic functions of 2, 3, and 4 variables

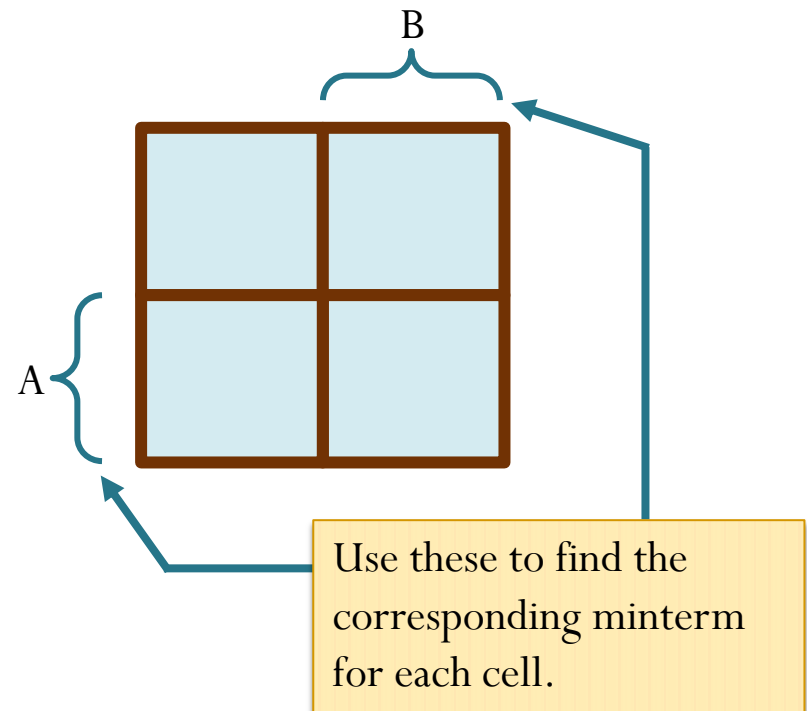
$$\text{Domain} = \{A, B\}$$

Each row of the truth table corresponds to one minterm.

Input Columns			Minterm
A	B	X	
0	0		$\overline{A} \cdot \overline{B}$
0	1		$\overline{A} \cdot B$
1	0		$A \cdot \overline{B}$
1	1		$A \cdot B$

Output Column

Each cell of the K-map corresponds to one minterm.



The 3-variable Karnaugh Map

Domain = $\{A, B, C\}$

$AB \backslash C$	0	1
00		
01		
11		
10		

(a)

$AB \backslash C$	0	1
00	$\bar{A}\bar{B}\bar{C}$	$\bar{A}\bar{B}C$
01	$\bar{A}B\bar{C}$	$\bar{A}BC$
11	$AB\bar{C}$	ABC
10	$A\bar{B}\bar{C}$	$A\bar{B}C$

(b)

The 4-variable Karnaugh Map

Domain = $\{A, B, C, D\}$

$AB \backslash CD$	00	01	11	10
00				
01				
11				
10				

(a)

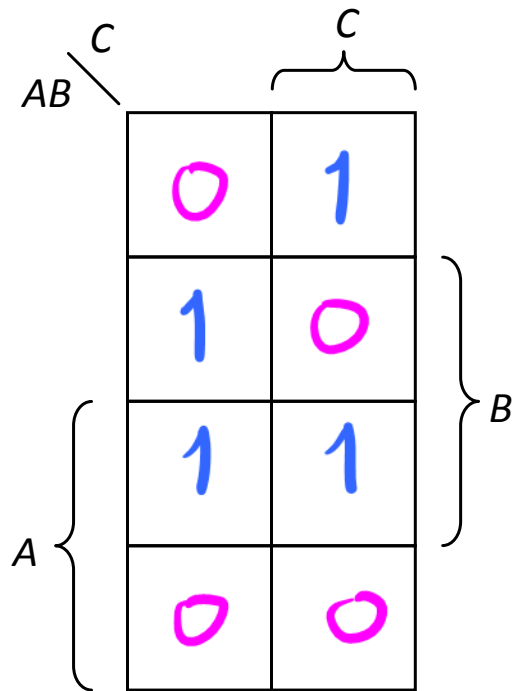
$AB \backslash CD$	00	01	11	10
00	$\bar{A}\bar{B}\bar{C}\bar{D}$	$\bar{A}\bar{B}\bar{C}D$	$\bar{A}\bar{B}C\bar{D}$	$\bar{A}\bar{B}CD$
01	$\bar{A}B\bar{C}\bar{D}$	$\bar{A}B\bar{C}D$	$\bar{A}BC\bar{D}$	$\bar{A}BCD$
11	$AB\bar{C}\bar{D}$	$AB\bar{C}D$	$ABC\bar{D}$	$ABCD$
10	$A\bar{B}\bar{C}\bar{D}$	$A\bar{B}\bar{C}D$	$A\bar{B}C\bar{D}$	$A\bar{B}CD$

(b)

Mapping (Canonical Sum)

Map the following expression on an appropriate Karnaugh map

$$\overline{A}BC + \overline{A}B\overline{C} + A\overline{B}C + ABC$$



$$\overline{A}BCD + \overline{A}B\overline{C}\overline{D} + A\overline{B}C\overline{D} + ABCD + A\overline{B}C\overline{D} + \overline{A}BC\overline{D} + \overline{A}B\overline{C}D$$

